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		\\$	a	A	ication Number	09/661,919	
l INFO	DRMATION D	ISCLOSŮ	ŘE.	Filin	g Date	September 14, 2000	
STATEMENT BY APPLICANT					Named Inventor	Taku Uchino	
					Init	2128	
(Use as many sheets as necessary)				Examiner Name		F. Ferris III	
Sheet	1	of	1	Atto	mey Docket Number	3180.0265	

U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS						
Examiner Initials	Cite No.1	Document Number	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number-Kind Code ² (if known)				
		US-				
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Note: Submission of copies of U.S. Patents and published U.S. Patent Applications is not required.

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (# known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation ⁶
		JP 9-245081	9/19/97	Nippon Telegraph & Telephone Corp.		Abstract
		JP 8-249372	9/27/96	Toshiba Corp.		Abstract
		JP 8-153127	6/11/96	Hitachi Ltd.		Abstract
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NON PATENT LITERATURE DOCUMENTS					
Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation ⁶			
	Notice of Grounds for Rejection issued by the Japanese Patent Office mailed April 11, 2006, for Japanese Patent Application No. 11-264538, and English-language translation thereof				
	UCHINO, "METHOD FOR ESTIMATING POWER CONSUMPTION OF AN INTEGRATED CIRCUIT," Toshiba Technology Publications (August 29, 1996), 14-46:1-2				
		Cite No.¹ Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. Notice of Grounds for Rejection issued by the Japanese Patent Office mailed April 11, 2006, for Japanese Patent Application No. 11-264538, and English-language translation thereof UCHINO, "METHOD FOR ESTIMATING POWER CONSUMPTION OF AN INTEGRATED CIRCUIT,"			

Examiner Signature	7-17-	Date Considered	5/24/06
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